



PATENT  
0717-0448P

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3/18/03

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Hiroki DOI et al. ✓ Conf.: 3457  
Appl. No.: 09/696,006 ✓ Group: 2858 —  
Filed: October 26, 2000 ✓ Examiner: LAIR, DONALD  
For: HALL DEVICE BIASING CIRCUIT &  
MAGNETISM DETECTION CIRCUIT INCLUDING  
THE SAME

**AMENDMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

March 12, 2003

Sir:

In response to the Office Action dated December 12, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

TECHNOLOGY CENTER 2800

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**IN THE CLAIMS**

Please amend the claims as follows:

1. (Amended) A Hall device biasing circuit, comprising a plurality of terminals for applying a bias voltage to a plurality of Hall devices connected in series, respectively, so that a driving current driving at least one Hall device of the plurality of Hall devices is a current adjusted amount of a driving current driving another Hall device through a corresponding terminal of the plurality of terminals.